

Developing software solutions and embedded systems is a challenge we simply ♥

If you do too, hop on!

Embedded FPGA Engineer main focus VHDL - all genders

Professional or Beginner Level

Ruggell, Liechtenstein or Vienna, Austria or Valencia, Spain

The Job

- Designing and developing embedded systems in exciting and demanding projects, mostly from scratch
- Technical challenges in various fields e.g. advanced data processing (object detection, FFT, ..) talking with peripherals and control all together
- Working directly with customers and reach their goals with your expertise
- Managing working time on your own while coordinating with your team and customers
- Working in an agile team where your opinion matters
- Supporting team members with your knowledge from system design to finding uncomfortable bugs

You ...

- are experienced in VHDL and have basic knowledge in electronics.
- are passionate about automation and high-quality code (CI/CD, Unit Testing).
- want to work with state-of-the-art and new technologies.
- You enjoy describing electronic circuits. Writing clean, reusable and maintainable code is a non-arguable condition for you.
- are open-minded, flexible and a team player.
- ideally have a university or technical college degree in Computer Engineering, electronics or a related discipline.

We Offer

- Challenging projects in a setup of distributed teams
- Actively managed personal development and educational trainings
- Five free days per year and financial extra budget for topics of interest
- Participation of several PHIOS benefits and events among our distributed PHIOS locations
- International cooperation and team exchange



Flexible working hours



Home office



Public transport



Events



Commissions

